



## Configuration Specs FCA-2200K

### 1. Activity LED (J7) and Drive Start-up Configuration Jumpers: J7

#### 1.1 Activity LED:

An external Activity LED can be installed across J7 pins 7 & 8. Make sure Pin 8 connects to the + (anode) of the LED and pin 7 connects to the – (cathode) of the LED. Do not insert external resistor in path of the LED. The LED activities are summarized below:

| Normal Command Activity                    | LED Status                                 |
|--|--|
| Spun down and no activity                  | Slow Blink (20% on 80% off a 2 sec. cycle) |
| Spun down and activity (command executing) | On   |
| Spun up and no activity                    | On   |
| Spun up and activity (command executing)   | Off  |
| Spinning up or down                        | Blinks steadily (50% on and 50% off)       |
| Format in progress, each cylinder change   | Toggles on/off                             |

1.2 Drive Start-up configuration jumpers: Installing jumpers across J7 1-2 (Start 1) and across J7 3-4 (Start 2) determines how the drive motor spins up. Start 1 and Start 2 is “low” if a respective jumper is installed and “high” if the jumper is not installed.

| Case | Start 2 (J7 3-4) | Start 1 (J7 1-2) | Motor Spin Function  |
|------|------------------|------------------|--|
| 1    | Low              | Low              | Motor spins up at DC power on.   |
| 2    | High             | Low              | Motor spins up only when Start command is received.  |
| 3    | Low              | High             | Motor spins up after a delay of 12 seconds times the modulo 8 value of the numeric SEL ID of the drive from DC power on. |
| 4    | High             | High             | The drive will not spin up.  |

### 2. Fibre Channel Cable Connections: (J1, J4, J5, J6)

The board supports two independent loops (Channel A and Channel B)

#### 2.1 Ch. A In (J1)

Connect the Output of previous Ch. A (Ch. A Out) to the “Ch. A In” connector J1.

If this is the first or only backplane (from the host controller), connect “RECEIVE” input cable from the host Controller to J1.

#### 2.2 Ch. A Out (J4)

Connect the Input to the next Ch. A (Ch. A In) to this backplane’s “Ch. A Out” connector J4. If this is the only of last backplane (from the host controller, connect “TRANSMIT” cable from the host Controller to J4.

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### 2.3 Ch. B In (J5)

Connect the Output of previous Ch. B (Ch. B Out) to the “Ch. B In” connector J5. If this is the first or only backplane (from the host controller), connect “RECEIVE” input cable from the host Controller to J5.

### 2.4 Ch. B Out (J6)

Connect the Input to the next Ch. B (CH. B In) to this backplane’s “Ch. B Out” connector J6. If this is the only or last backplane (from the host connector), connect “TRANSMIT” cable from the host Controller to J6.

## 3. FC Drive Connector (J2)

The hot swappable FC Hard Disk Drive is connected to J2.

## 4. Power Connection (J3)

Connect 4-pin power cable from the power supply to this connector. Pin 1 is assigned for +12V, and pin 4 for +5V

## 5. 2GHz/1GHz selection and Drive ID Selection JB1

### 5.1 2GHz/1GHz Selection: JB1 (X-Y)

Pins labeled JB1 X-Y determine the FC transfer speed. When a jumper is installed across JB1 X-Y, the board is capable to support transfer rate of 2.125 GHz on both ports. If the jumper is not installed across X-Y, the board supports only 1.0625GHz operation on each port.

## 6. SEL\_6 through SEL\_0 ID lines (JB1 1-14, 2-13, etc.)

The SEL\_6 through SEL\_0 ID lines determine drive address as follows:

| <b>JB1<br/>1-14</b> | <b>JB1<br/>2-13</b> | <b>JB1<br/>3-12</b> | <b>JB1<br/>4-11</b> | <b>JB5-10</b> | <b>JB1<br/>6-9</b> | <b>JB1<br/>7-8</b> | <b>SEL ID<br/>(HEX)</b> |
|---------------------|---------------------|---------------------|---------------------|---------------|--------------------|--------------------|-------------------------|
| OFF                 | OFF                 | OFF                 | OFF                 | OFF           | OFF                | OFF                | 0                       |
| OFF                 | OFF                 | OFF                 | OFF                 | OFF           | OFF                | ON                 | 1                       |
| OFF                 | OFF                 | OFF                 | OFF                 | OFF           | ON                 | OFF                | 2                       |
| OFF                 | OFF                 | OFF                 | OFF                 | OFF           | ON                 | ON                 | 3                       |
| .....               | .....               | .....               | .....               | .....         | .....              | .....              | .....                   |
| ON                  | ON                  | ON                  | ON                  | ON            | OFF                | OFF                | 7C                      |
| ON                  | ON                  | ON                  | ON                  | ON            | OFF                | ON                 | 7D                      |

NOTE: Valid addresses are limited to hex 7D only.

Also, please note that the AL\_PA (Arbitrated Loop Physical Address) values are mapped to the Selection ID values by the Drive Supplier as specified in the Drive Specifications.

8/1/04

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