

# Absolute Analysis Investigator™

## 10Gbps Ethernet Protocol Analyzer

### Product Specifications

Speed	10Gbps LAN/WAN/SAN/MAN
Number of Ports	Two ports per test card. (Capable of analyzing one full duplex link or two half duplex links.)
Max. Ports per Chassis	Up to 16
Line Rate	10Gbps Ethernet LAN, 10Gbps Ethernet WAN
Physical Interface	Industry standard SFP+. Shipped with 850nm SFP+.
Analyzer	Each analyzer has its own triggering and filtering resources
Traffic Capture	Each analyzer captures bidirectional traffic between two devices, or from up to two SPAN ports, or from up to two passive tap inputs.
Memory Size	2GB per port; 4GB per analyzer; 4GB per test card
Trigger	Multi-state, multi-level trigger sequencer per analyzer
Multi Channel	Trigger on sequence of events in any Rx channel, in any analyzer
Sequencer	32 states per sequencer
Resources	32 counters and timers per sequencer
Pattern Matchers	Eight 32-byte pattern matchers (primitives or frames) with associated local occurrence counters
Combinations	'OR' conditions between pattern matchers
Cross Triggering (internal)	Cross-module arm in/out for inclusion in sequencer events coming from another analyzer
Cross Triggering (external)	One external trigger in and one external out per test card
Filters	Hardware filter conditions can be defined individually for each sequence level
Error Detection	Disparity, code violation, CRC error, loss of sync
Frame Slicing	Beginning at the SOF, specified number of words are captured to memory – effectively deepening the trace memory
Resolution	10ns timestamp resolution
Time Correlation	All analyzers share the same clock to allow for time-correlated measurements. Time correlation on up to 16 ports.
Programming Languages	C/C++ API

For more info contact your Local Rep:

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